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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Donald L. Freerksen, et al. : Date: April 08, 2005  
Serial No.: 09/866,981 : IBM Corporation  
Group Art Unit: 2187 : Intellectual Property Law  
Examiner: Bao Q. Truong : Dept. 917, Bldg. 006-1  
Filed: May 29, 2001 : 3605 Highway 52 North  
Patent No.: 6,871,267 B2 : Rochester, MN 55901-7829

Title: METHOD FOR INCREASING  
EFFICIENCY IN A MULT-PROCESSOR  
SYSTEM AND MULTI-PROCESSOR  
SYSTEM WITH INCREASED  
EFFICIENCY

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

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being deposited with the United States Postal  
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(Date of Deposit)

Susan K. Berge  
Susan K. Berge

PETITION FOR CERTIFICATE OF CORRECTION

In the matter of Patent Number 6,871,267 B2 issued on Mar. 22, 2005, a careful check of  
the same against the file of the attorney shows errors in the patent chargeable to the Official  
Printer, as shown on the attached Certificate of Correction.

It is requested that the attached Certificate of Correction be certified and issued to  
International Business Machines Corporation, Rochester, Minnesota 55901.

Respectfully submitted,

By Robert R. Williams  
Robert R. Williams, Patent Agent  
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Docket No.: ROC919970187US2  
Serial No.: 09/866,981

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : US 6,871,267 B2

DATED : Mar. 22, 2005

INVENTOR(S) : Donald Lee Freerksen and Gary Michael Lippert

It is certified that an error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Claim 11, Col. 16, Line 51, "end" should be --and--.

MAILING ADDRESS OF SENDER:

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PATENT NO. \_\_\_\_\_

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